



International Journal on Recent Researches In Science, Engineering & Technology

A Journal Established in early 2000 and upgraded to International journal in 2013 and is in existence for the last 10 years. It is run by Retired Professors from NIT, Trichy. It is an absolutely free (No processing charge No publishing charge etc) Journal Indexed in DIIF and SJIF.

Research Paper

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ISSN (Print) : 2347-6729
ISSN (Online) : 2348-3105

**Volume 1, Issue 12,
Dec. 2013**

**DIIF IF :1.46
SJIF IF: 1.329**

On Chip Data Encoding

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Abstract : Literature review revealed that VLSI circuit design consuming low power become necessary for present technologies. One way of reducing dynamic power consumption in a CMOS circuit is to reduce the number of switching activity. In fact, self-switching and coupling switching activity is responsible for link power dissipation. The aim of low power data encoder is to transmit the data on the bus with reduced switching activity. In this paper the improved form of bus invert coding is implemented which is based on odd line and even line invert condition. Odd and even line condition is used to minimize the unwanted transitions on the data path which provides better results in terms of power dissipation and switching activity when compared to traditional bus-invert method.